change +2.5v to 2.5vsus

MONTARA-GME/ 855GM+ 3 of 3 (POWER & CAP.)
Using short/wide trace.

Connect to Pin 33 ground close to chip.

Set GPIO[1:0] Pull Up and Pull Down TV output option will be defined later by SW group.
5 IN1 CARD READER
The AMC20493-001 modem is used for mother board family MBAMC20493-010.
0312 Gain Table

<table>
<thead>
<tr>
<th>GAIN</th>
<th>GAIN1</th>
<th>SE/BTL</th>
<th>AV (inv)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>6 dB</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>10 dB</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>15.6 dB</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>21.6 dB</td>
</tr>
<tr>
<td>X</td>
<td>X</td>
<td>1</td>
<td>4.1 dB</td>
</tr>
</tbody>
</table>
C926 must be placed near pin 26 (CLK).

C930 must have max. ESR of 2-ohms.

C928, C930 and C972 must be placed near pins 2 (AVdd) and 6 (AGnd).

Circuit traces for C922 and C924 should be less than 2 inches.

C922 and C924 must be Y3 type Capacitors for Nordic Countries only.

C944, C974 and C976 must be placed near pins 3 (Vc) and 4 (VRef).

C970 & C982 must be placed near pins 7 (PWR+) and 6 (AGnd).

Layout trace short.
For 8100B

2/5 FROM PWS
Rev. B. 10/02

12/15 FROM PWS
Rev. B. 10/02

RT21100B/8100C

PROJECT : CT1
Quanta Computer Inc.

Date: Sheet Size Document Number Rev

23 38 Wednesday, April 14, 2004
1. AMD 29LV081B require MAX 500nS Tready for its hardware reset. And MAX6326_UR29 has >100mS reset timing. So we can tie its reset# pin to +3VALW directly.

2. SIO has internal 20mS delay of VCC1_PWR0K
From Docking CNN

BATTERY CHARGER

ADAPTER 18.5V 65W 3.51A

Charge Current=VICTL*1.24

I source_Max=Vcls/0.8