<table>
<thead>
<tr>
<th>Pin</th>
<th>Pin Name</th>
<th>Signal Name</th>
<th>Type</th>
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<tbody>
<tr>
<td>5</td>
<td>GP0</td>
<td>VSUS питания</td>
<td>O</td>
</tr>
<tr>
<td>34</td>
<td>GP1</td>
<td>USB_0 питания</td>
<td>O</td>
</tr>
<tr>
<td>85</td>
<td>GP2</td>
<td>CPUPWR питания</td>
<td>O</td>
</tr>
<tr>
<td>69</td>
<td>GP3</td>
<td>PM_PWRDN</td>
<td>O</td>
</tr>
<tr>
<td>70</td>
<td>GP4</td>
<td>SUSC питание</td>
<td>O</td>
</tr>
<tr>
<td>75</td>
<td>GP5</td>
<td>SUSC питания</td>
<td>O</td>
</tr>
<tr>
<td>105</td>
<td>GP7</td>
<td>PM_PWRDN</td>
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<tr>
<td>148</td>
<td>GP9</td>
<td>IC3_PWRDN</td>
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<tr>
<td>149</td>
<td>GP10</td>
<td>SUSC питanie</td>
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<td>152</td>
<td>GP13</td>
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</tr>
<tr>
<td>153</td>
<td>GP14</td>
<td>SUSC питanie</td>
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</tr>
<tr>
<td>155</td>
<td>GP15</td>
<td>CHG_0 питanie</td>
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<tr>
<td>158</td>
<td>GP16</td>
<td>PRECHG питanie</td>
<td>O</td>
</tr>
<tr>
<td>168</td>
<td>GP18</td>
<td>BAT питания</td>
<td>O</td>
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<tr>
<td>174</td>
<td>GP24</td>
<td>BAT питание</td>
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</table>

**PCI Device**

<table>
<thead>
<tr>
<th>IDSEL#</th>
<th>REQ/GNT#</th>
<th>Interrupts</th>
</tr>
</thead>
<tbody>
<tr>
<td>AD17</td>
<td>0</td>
<td>INTA-&gt;INTB</td>
</tr>
<tr>
<td>AD16</td>
<td>2</td>
<td>INTA-&gt;INTB</td>
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**SM-Box Device**

<table>
<thead>
<tr>
<th>SM-Box Address</th>
<th>Engine: AsusTek Computer INC.</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Controler: 2.0G Z84FM</td>
</tr>
<tr>
<td></td>
<td>Date: 25 7 Thursday, October 05, 2006</td>
</tr>
</tbody>
</table>

**Title:** EC GPIO SETTING

**Engineer:** Ken Chen
PCB_ID[2:0] 000: R1.0

From +5vs change to +12vs for V1.02G
From +3vsus change to +3vs for V1.02G
From 10K change to 4.7K for V1.02G

06/03/30, refer Z96J R1.01 to delete and change net name from VRMPWRGD to VRM_PWRGD.
PLACE ESD Diodes near VGA port

- +5VS Change to +3VS
- Remove 2N7002
- 22pF/25V change to 15p/50V FOR EMI

For EMI solution
Custom
**INVERTER Interface/Speaker Conn.**

**BIOS**

BACK_OVF: When user push "Fn+4" button, BIOS active this pin to turn off backlight.

**Panel ID1**: 1: WXGA+ 1680x1050  
Panel ID0: 0: WXGA 1280x800  
Panel ID0 Reserve for vendor

---

**Cable Requirement:**

- Impedance: 150 ohm +/- 10%  
- Length Measurement: 10 cm/10 cm  
- Twisted Pair/Net Ribbon  
- Maximum Length: 18"
PLACE ESD
Diodes near
TV port
Route H_THERMDA and H_THERMDC on the same layer.

- Max: 1mA
- Close to Pin A24 & A25 of CPU

\[ \text{OTHER SIGNALS} \]

\begin{align*}
&15 \text{ mils} = \text{GND} \\
&10 \text{ mils} = \text{H_THERMDA} (10 \text{ mils}) \\
&10 \text{ mils} = \text{H_THERMD} (10 \text{ mils}) \\
&10 \text{ mils} = \text{GND} \\
&15 \text{ mils} = \text{OTHER SIGNALS}
\end{align*}

Avoid FSB, Power DC FAN Control

Remove Fan control circuit

CPU FAN will be forced on:
1) Thermal Sensor Over-temperature
2) WATCHDOG asserted by EC
Reserved R to +3VSUS for Wake on WLAN function!
No Mount

SMBCLK to SMBDATA
SMBDATA to +1.5V

!! ExpressCard Standard 1.0:
Change Pin 7 from RESERVED to SMBCLK
Change Pin 8 from SMBCLK to SMBDATA
Change Pin 9 from SMBDATA to +1.5V

change part to Lead free
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**ISA ROM**

**EC Hardware Strapping**

00: PNP/CNG Access Register Pair Are 002Eh and 002Fh
10: PNP/CNG Access Register Pair Are 004Eh and 004Fh
11: Reserved
01: PNP/CNG Access Register Pair Are Determined by EC Domain Registers SWCBALR and SWCBAHR.

0: KBS Interface Pins Are Switched to Parallel Port Interface for In-System Programming
1: Normal

0: Disable Shared Memory with Host BIOS
1: Enable Shared Memory with Host BIOS

Note: Sampled at VSTBY Power Up Reset
Without Battery & Pull out Adapter

remove AC DC detect; no need
For TPM Module

Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT

For EMI solution

Custom
For Bluetooth

Remove Side SW

USB_PP413
USB_PN413

BT_ON# 14

GND
+3V

USB_PP4
USB_PN4

BT_LED1
BT_LED2

BT_ON/OFF#

USB_PP4
USB_PN4

+3V
GND

RN76A 0Ohm
RN76B 0Ohm

C485
0.1UF/16V

R302
10KOhm

Q65
H2N7002

BT_CHD/LK28
BT_CHDAT28

CON31
WTOB_CON_10P

SIDE1
SIDE2

T64 Do Not Stuff

L64 Do Not Stuff

C480
0.1UF/16V

R302
10KOhm

Q65
H2N7002

BT_CHD/LK28
BT_CHDAT28
<table>
<thead>
<tr>
<th>Rev</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>08/04/06</td>
<td>Refer Z84F R2.2G</td>
</tr>
<tr>
<td>1.0</td>
<td>08/04/06</td>
<td>1. Change PATA to SATA&lt;P42&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/08/06</td>
<td>2. Modify Audio circuit for Jack-sense&lt;P33 &amp; P34&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/08/06</td>
<td>3. Add D40,D41,D42 for Battery variance&lt;P40&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/08/06</td>
<td>4. Modify inverter connector pin define&lt;P20&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/08/06</td>
<td>5. Change CON5 Connector from 10pin to 12pin &lt;p20&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/10/06</td>
<td>6. ADD PR5125,PR8126 for DDR8/VTT on S3 issue&lt;PS0&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/10/06</td>
<td>7. Change CON21 touch pad connector type&lt;P39&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/10/06</td>
<td>8. Change J2 connector type for Azalia &lt;P34&gt;</td>
</tr>
<tr>
<td>1.0</td>
<td>08/10/06</td>
<td>9. Add C484,C485,C486,C487 for EMI Solution&lt;P32&gt;</td>
</tr>
<tr>
<td>2.0</td>
<td>08/10/06</td>
<td>10. Change CON21 touch pad connector type&lt;P39&gt;</td>
</tr>
<tr>
<td>2.0</td>
<td>08/10/06</td>
<td>11. Change R40 to 4.99K to 4.87K for Macro Vision issue&lt;P7&gt;</td>
</tr>
<tr>
<td>2.0</td>
<td>08/16/06</td>
<td>12. Add D43 and Del R422 for MIC2_VREFOUT issue&lt;P33&amp;P34&gt;</td>
</tr>
<tr>
<td>2.0</td>
<td>09/10/06</td>
<td>13. R408 from 24.9 ohm change to 20 ohm for SATA eye pattern issue&lt;P12&gt;</td>
</tr>
<tr>
<td>2.0</td>
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<td>14. PC5005, PC5017 from 0.1uF change to 0.22uF for Power solution&lt;p49&gt;</td>
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<tr>
<td>2.0</td>
<td>09/13/06</td>
<td>15. Add C488,C489,C490,C491,C492 and C493 for EMI solution&lt;P42&gt;</td>
</tr>
<tr>
<td>2.0</td>
<td>09/13/06</td>
<td>16. C481, C482 and C483 from FN change to load for EMI solution&lt;P42&gt;</td>
</tr>
</tbody>
</table>

**Notes:**
- Refer Z84F R2.2G
- Change CON5 Connector from 10pin to 12pin
- Modify Audio circuit for Jack-sense
- Add D40,D41,D42 for Battery variance
- Modify inverter connector pin define
- Change J2 connector type for Azalia
- Add C484,C485,C486,C487 for EMI Solution
- Change R40 to 4.99K to 4.87K for Macro Vision issue
- Add D43 and Del R422 for MIC2_VREFOUT issue
- R408 from 24.9 ohm change to 20 ohm for SATA eye pattern issue
- PC5005, PC5017 from 0.1uF change to 0.22uF for Power solution
- Add C488,C489,C490,C491,C492 and C493 for EMI solution
- C481, C482 and C483 from FN change to load for EMI solution
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